

## MARIO NIEBLA

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### SUMMARY

- + Proven technical/people leader with global cross-functional devops engineering organizations
- + Highly experienced in reliability, observability, libraries, CIAM and security services and products
- + Passionate about high scale, highly available mission critical cloud microservices and infrastructure

### EXPERIENCE

- |                        |  |                                  |
|------------------------|--|----------------------------------|
| Jul 2025 -<br>CURRENT  | <b>CONFLUENT</b><br><b>Senior Director of Engineering, Security Platform</b>   | <b>Walnut Creek, CA (remote)</b> |
|                        | <ul style="list-style-type: none"><li>+ Engineering leader for 60 across US and Canada developing Customer &amp; Internal Security services</li><li>+ Responsible for Customer IAM and Infrastructure security (SPIFFE/SPIRE implementation and internal access control &amp; transparency)</li></ul>  |                                  |
| Jul 2023 -<br>Jul 2025 | <b>WORKDAY</b><br><b>Senior Director of Engineering, Identity and Access Management</b>  | <b>Encinitas, CA (hybrid)</b>    |
|                        | <ul style="list-style-type: none"><li>+ Engineering leader for org of 80 across US and EU developing CIAM backend and frontend applications</li><li>+ Responsible for incremental and re-platforming efforts around SSO, OAuth, Federated Identity, threat-intelligence, and M&amp;A integration programs.</li><li>+ Drove "Engineering Excellence" programs including a focus on resilience, metrics, testing and incident management improving availability and engineering practices.</li><li>+ Chaired working group to pivot platform-wide technical strategy retaining strategic partnership.</li><li>+ Implemented "top 10 risks" program to highlight and address top operational risks.</li></ul>   |                                  |
| Sep 2021 -<br>Nov 2022 | <b>TWILIO</b><br><b>Senior Director of Engineering, Identity and Access &amp; Edge Platforms</b>   | <b>Encinitas, CA (remote)</b>    |
|                        | <ul style="list-style-type: none"><li>+ Engineering leader for org of 90 across US, EU, and India developing APIs and frontend</li><li>+ Responsible for all Edge Infrastructure (API &amp; Web Gateways, ALBs/NLBs, WAF, DDoS Protection)</li><li>+ Responsible for all Customer IAM products (Accounts, Organizations, Users, Login, RBAC, SSO, OAuth, Data Signing &amp; Encryption)</li><li>+ Owned and operated 100+ high scale, high availability, mission critical microservices and applications</li><li>+ Reduced risk of high-impact incidents by driving blast radius reduction program across tier-0 services</li><li>+ Developed technical strategy and founded working group to unify customer identity across Twilio's acquired companies</li><li>+ Reduced COGs by 25% by running cost per million requests program</li><li>+ Drove cross-BU program to adopt modern, granular access controls in key products</li></ul> |                                  |
| Oct 2018 -<br>Sep 2021 | <b>TWILIO</b><br><b>Director of Engineering, Identity and Access Platform</b>  | <b>San Francisco, CA</b>         |
|                        | <ul style="list-style-type: none"><li>+ Engineering leader for org of 50 across US, EU, and India developing APIs and frontend</li><li>+ Responsible for all Customer IAM systems (Accounts, Organizations, Users, Login, RBAC, SSO, Data Signing &amp; Encryption)</li><li>+ Hired Twilio's first engineering teams in India and within a year brought them on par with established teams in EU and US</li><li>+ Improved success rate of most critical services to (5) 9s by driving "operational audit" programs</li><li>+ Successfully drove IAM regionalization program enabling region-specific revenue in US1, AU1, IE1</li><li>+ Reduced latency by 25% and CPU utilization by 30% on key services by designing a multi-level distributed cache system</li></ul>   |                                  |

- Jan 2016 -  
Oct 2018
- TWILIO** **San Francisco, CA**  
**Software Architect, Identity and Access Platform**  
 + Designed and drove GDPR/Data Privacy effort for Customer IAM data/services  
 + Design/implementation of highly available, resilient cloud services and libraries using async-retry, circuit-breakers, fallback mechanisms, caches, etc.  
 + Designed novel RBAC system over complex platform with thousands of existing distinct datastores and ten's of billions of resources  
 + Technical oversight/leadership of engineering team of 35
- Jan 2014 -  
Jan 2016
- TWILIO** **San Francisco, CA**  
**Principal Engineer, Platform Services**  
 + Designed and implemented core services (API-Gateway, Accounts, API-Authentication, Feature-Bits, Credentials-Store, Data Signing)  
 + Developed service framework used by 75% of JVM-based services in Twilio  
 + Implemented highly available self-hosted memcached libraries/services with failover  
 + Technical oversight/leadership of engineering team of 15
- Dec 2011 -  
Dec 2013
- ZINIO** **San Francisco, CA**  
**Software Architect, Core Platform**  
 + Drove API redesign effort resulting in a single digit ms latency JSON based REST APIs  
 + Implemented quality-based software build pipeline, MAVEN, JENKINS, NEXUS, SONAR  
 + Performance, scalability and reliability tuning and optimization of REST APIs  
 + Implemented event-driven API proxy, NGINX, LUA
- Jul 2011 -  
Dec 2011
- VIADEO** **San Francisco, CA**  
**Software Architect, Core Platform**  
 + Design and development of professional social networking web platform (+40M users) SPRING, JSON, REST, HADOOP, HBASE, MEMCACHED, RABBITMQ
- Apr 2010 -  
Jul 2011
- ZINIO** **San Francisco, CA**  
**Technical Lead, Content Platform**  
 + Lead for digital, interactive content platform in SPRING, STRUTS, JSP, XQUERY, SQL, XML, RESTful Web Services  
 + Developed multi-platform, interactive content markup language in JAVA, XML
- Apr 2004 -  
Apr 2010
- TARARI/LSI (acquired by Broadcom)** **San Diego, CA**  
**Software Engineer, Content Processor Group**  
 + Performance analysis and integration for SAP Enterprise Service Bus hardware acceleration project in C, and J2EE  
 + Developed tools for lexical/semantic analysis of XML, XPath and RegEx in C  
 + Tuning, analysis and optimization of high-performance XML libraries in C, JNI, JAVA  
 + Implementation and maintenance of APIs and algorithms related to FPGA-based XML/RegEx processors C, JNI, JAVA

## **EDUCATION**

- + University of California - San Diego, B.S. Computer Engineering, 2003
  - 4 quarters of undergraduate research in VLSI test

## **OTHER**

- + US Citizen
- + Fluent in English, Spanish